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<u>L13</u>	L12 and 12	2	<u>L13</u>
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<u>L11</u>	L10 and 14	0	<u>L11</u>
<u>L10</u>	L9 same 12	19	<u>L10</u>
<u>L9</u>	pld	4347	<u>L9</u>
<u>L8</u>	L7 and 11	14	<u>L8</u>
<u>L7</u>	L6 same 12	40	<u>L7</u>
<u>L6</u>	scan adj 1 chain	1250	<u>L6</u>
<u>L5</u>	L4 and 13	3	<u>L5</u>
<u>L4</u>	bist	1426	<u>L4</u>
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<u>L2</u>	(core or embedded) adjl logic	1774	<u>L2</u>
<u>L1</u>	fpga	6202	<u>L1</u>